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10/693,215

PATENT
Conf. No.: 2277

IN THE UNITED STATES PATENT OFFICE

Applicants: Michael A. Nix et al.
Assignee: Xilinx, Inc.
Title: Method and Circuit for Folded Analog-to-Digital Converter (ADC) Using Frequency Detectors and Time Detectors

Serial No.:	10/693,215	File Date:	10-24-03
Examiner:	Peguy J. Pierre	Art Unit:	2819
Docket No.:	X-1064-1D US	Conf. No.:	2277

COMMISSIONER FOR PATENTS
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on March 10, 2004, please replace/substitute the following claims as indicated.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.